

Preview

Artificial perception system for dynamic recognition and trajectory extraction

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Multi-modal in-sensor computing offers a promising approach to enhancing data processing efficiency and achieving breakthroughs in bandwidth and energy consumption. In a recent study published in *Device*, Chang et al. introduced a porous-Si/Si artificial visual system created through electrochemical etching. The voltage regulation performance, driven by the photovoltaic effect, has the potential to inspire the development of next-generation neuromorphic devices.

The visual system is essential for the survival of living organisms, serving as a primary means of gathering environmental information. It allows organisms to perceive various visual cues, such as light, color, shape, motion, and depth, all crucial for survival and reproduction. Over millions of years, biological vision has evolved to meet the specific survival needs of different species. To overcome the limitations of natural vision, machine vision, inspired by biomimicry, has been developed and is advancing rapidly. Combined with deep learning and artificial intelligence, machine vision systems have great potential for integrating spatial and temporal data streams.¹ For machine vision that draws inspiration from biology and aims to surpass it, the primary applications are autonomous driving, industrial automation, robotics, and medical imaging. Consequently, broadband in-sensor processing and dynamic sensing are critical.

In a recent issue of *Device*, Chang et al. introduced an innovative approach to integrating dynamic recognition and trajectory extraction.² Their use of a porous-p-Si/p-Si heterojunction, created through electrochemical etching, features a strong built-in electric field that efficiently separates photo-generated electron-hole pairs, resulting in a rapid photoelectric response within microseconds (Figure 1A). Notably, the device's differential photoelectric response was leveraged to extract the trajectory of light sources.

The source-drain voltage difference, caused by the irradiation of a visible spot at various locations in the device channel, allows for capturing the spot's movement path. This position-dependent differential voltage naturally oscillates between positive and negative, paving the way for further integration of edge enhancement algorithms.

Some pioneering efforts have focused on the neuromorphic response and in-sensor computing of devices by incorporating various functional layers, such as ferroelectric, phase change, and thermoelectric materials. Among these, constructing heterojunctions has been a complex choice due to challenges like large-area integration, device uniformity, and compatibility with current complementary metal-oxide semiconductor (CMOS) integration processes.³ Introducing defect energy levels for carrier trapping and release offers another pathway to realizing a bionic vision system, especially for large-area channel preparation.⁴ In Chang's study, electrochemical etching created a porous-p-Si layer on the p-Si surface. The etching process can be precisely controlled by adjusting the current level and etching time, while large-area etching ensures device uniformity. The entire device, utilizing silicon as the channel material, is compatible with current chip fabrication processes, offering the potential for large-scale integration.

Traditionally, sensing systems are physically separate from computation units due

to differing usage requirements and practical process limitations. As a result, sensor units capture large amounts of raw data locally, which must then be transferred to a local computing unit or a cloud-based system.⁵ Meanwhile, computations are typically conducted digitally using traditional von Neumann computing architectures, which lack parallel computation capabilities. This process poses significant energy consumption, response time, data redundancy, communication bandwidth, and security challenges. In an in-sensor computing architecture, individual sensors or integrated sensor arrays can directly process environmental information, eliminating the need for constant data exchange between the sensor and processor, thereby combining sensing and computational functionality.⁶ Thus, in Chang's work, energy band bending due to the porous-p-Si/p-Si heterojunction enabled a real-time optical signal response, ensuring effective front-end signal collection. Unlike other approaches, this work achieved real-time localization of the light source signal position. With a nearly closed four-terminal electrode structure, the trajectory of the light source can be effectively pinpointed using the voltage difference between opposing electrodes. The inherent directionality of the voltage difference even allows for identifying the light source's movement path, including its direction and pattern. Figures 1B and 1C illustrate the change in voltage difference between two pairs of electrodes as the laser spot moves



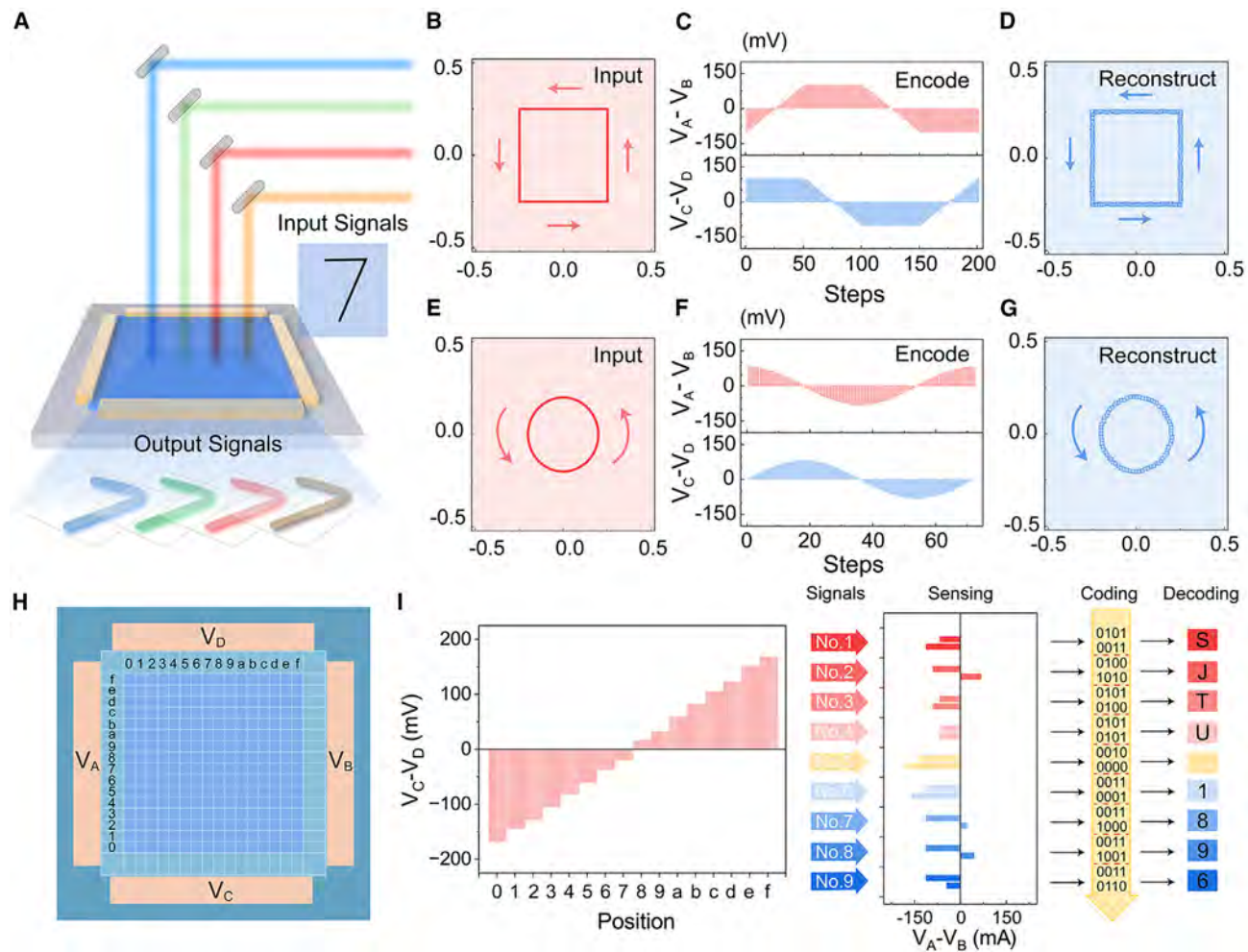


Figure 1. The structure and optoelectronic working processes of the device

- (A) Schematic diagram of device structure and broadband sensing.
 (B) Square moving trajectory at optical signal input.
 (C) Voltage difference between opposite electrodes with the square moving trajectory.
 (D) Reconstructing the square trajectory of the moving light source.
 (E) Circular moving trajectory at optical signal input.
 (F) Voltage difference between opposite electrodes with the circular moving trajectory.
 (G) Reconstructing the circular trajectory of the moving light source.
 (H) Schematic diagram of device sensing area division with 8-bit information coding.²
 (I) The relationship between the vertical position of the laser spot and the voltage difference and the schematic diagram of the sensing and encoding process.
 Adapted from Chang et al.²

in a counterclockwise square. The optical signal's trajectory can then be reconstructed (Figure 1D). This perception and reconstruction process is also effective for signals from circular moving trajectories (Figures 1E–1G).

To further advance the in-sensor computing process, devices continually evolve toward multi-dimensional sensing, finer front-end processing, easier integration and fabrication, faster response times, and lower power consumption. At the front-end perception level, multi-modal

perception is a priority for devices to capture external information. Currently, biomimetic devices based on biological perception primarily imitate a few key functions. However, for example, in visual bionics, the visual sensing wavelength of certain organisms is often broad. Simultaneously, the visual system can sense multi-dimensional information such as temperature, humidity, gas, and pressure. Therefore, broadband sensing is a vital feature of visual sensing devices. Compared to materials with relatively

poor environmental stability, such as perovskite, black phosphorus, and organic semiconductor films, the porous-p-Si/p-Si heterojunction demonstrated stable detection from ultraviolet to near-infrared in the air atmosphere.⁷ Beyond information perception, improving signal-to-noise ratio, suppressing background noise, and enhancing feature signals are also critical. Effective signal extraction can significantly reduce computational load and increase the efficiency of high-level processing tasks. In Chang's work,

the device was divided into 20 regions based on the laser spot size (Figure 1H). In this quadrilateral electrode structure, 8-bit binary signals can be encoded by combining the respective encoding capabilities of the voltage difference between the transverse and longitudinal electrodes (Figure 1I). Achieving this position-dependent multi-bit writing capability hinges on the apparent signal difference between neighboring electrodes. In this study, the potential difference between adjacent intervals exceeded 20 mV, ensuring accurate signal recognition.

High integration is the future direction for device development, encompassing materials, devices, architectures, and algorithm co-design. The memristor offers a viable concept, with classical crossbar array structures easily integrated at high density into CMOS-compatible processes. This two-dimensional transverse and longitudinal structure can naturally perform vector-matrix multiplication, one of the most important and frequent operations in machine learning. As data are stored and transmitted by the array, conductance can be multiplied and summed under Ohm's law and Kirchhoff's current law.⁸ Another emerging strategy is integrating resistive switching devices with three-terminal transistor devices. Integrating one transistor and one memristor (1T1R) or multiple transistors and one memristor (MT1R) can enhance the device's computational dimension. When equipped with threshold switches, devices of this structure offer a broader range of neural network compatibility.⁹ Arrays of in-sensor computing devices based on dynamic recognition and trajectory extraction also hold the potential for further integration and versatility. Smaller device partitions and the integration of

resistive switches provide strategies for subsequent development.

After decades of reliance on von Neumann architectures, we are transitioning into the next era. With the rapid development of neuromorphic computing, we need new approaches to handle the burgeoning data processing demands and computing paradigms ahead. In-sensor computing is currently a crucial development direction and a key breakthrough from existing architectures. Combining large-area, multi-component integration and multi-modal sensing allows for more sophisticated adaptive recognition capabilities under low-energy conditions. In this work, the combination of in-sensor processing and real-time trajectory recognition demonstrates the ability for position-dependent multi-bit coding with broadband detection, showcasing potential applications in smart driving, robotics, and intelligent identification.

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DECLARATION OF INTERESTS

The authors declare no competing interests.

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